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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10053865	01/18/2002	257	4.1	2811	<u> </u> <i>lm</i>

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**CONTINUING DATA VERIFIED: None J42

** FOREIGN APPLICATIONS VERIFIED:

EUROPEAN PATENT OFFICE (EPO) 01830031.9 01/22/2001 J42

PG-PUB ☐ DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed ☐ yes ☐ no
35 USC 119 conditions met ☐ yes ☐ no
Verified and Acknowledged Examiners's initials

ATTORNEY DOCKET NO

00-CT-320

TITLE : Silicon improved schottky barrier diode

U.S. DEPT. OF COMM./PAT. & TM.-PTO-4361 (Rev. 12-84)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
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